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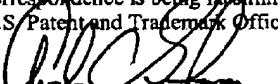
Title:	MULTIPLE TWIN CELL NON-VOLATILE MEMORY ARRAY AND LOGIC BLOCK STRUCTURE AND METHOD THEREFOR		
Application No.:	10/675,212	Filed:	September 30, 2003
Examiner:	Ho, Hoai V.	Group Art Unit:	2818
Atty. Docket No.:	023-0024	Confirmation No.:	9946

ATTACHED HERETO:

(1) Response to the Non Final Office Action dated October 13, 2004 (8 pages)

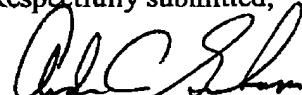
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Respectfully submitted,



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PATENT

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Applicant(s): Roy E. Scheuerlein, et al.

JAN 13 2005

Title: MULTIPLE TWIN CELL NON-VOLATILE MEMORY ARRAY AND
LOGIC BLOCK STRUCTURE AND METHOD THEREFOR

Application No.: 10/675,212

Filed: September 30, 2003

Examiner: Ho, Hoai V.

Group Art Unit: 2818

Atty. Docket No.: 023-0024

Confirmation No.: 9946

January 13, 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO NON-FINAL OFFICE ACTION

This paper is responsive to the Non-Final Office Action mailed on October 13, 2004, having a shortened statutory period for response set to expire January 13, 2005. In light of the Amendments and/or Remarks herein, further examination is requested.